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U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub Class

FOREIGN PATENT DOCUMENTS

Exam Init	Ref	Document Number	Publication Date	Country	Name

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)
		Article from IEEE Journal of Solid-State Circuits, Vol. 29 No. 1, pp. 9-13 January 1994, entitled "OFFSET COMPENSATING BIT-LINE SENSING SCHEME FOR HIGH DENSITY DRAM'S"

Examiner: M. TRAN

Date Considered: 6/22/04